

WEST Search History

DATE: Tuesday, January 04, 2005

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			<i>DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L12		(debug\$6 with (keyboard with controller\$2))	30
<input type="checkbox"/>	L11		(710/100, 107, 110, 305-306).ccls.	3942
			<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L10		(debug\$6 with (keyboard with controller\$2))	3
			<i>DB=USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L9	L8	and @pd > 20040729	0
<input type="checkbox"/>	L8	L6	and (keyboard with controller\$2)	9
<input type="checkbox"/>	L7	L6	and (keyborad with controller\$2)	0
<input type="checkbox"/>	L6	L5	and L3	62
<input type="checkbox"/>	L5		(debug\$6 with controller\$2)	1102
<input type="checkbox"/>	L4	L2	and L3	3
<input type="checkbox"/>	L3	717/	\$.ccls.	4744
<input type="checkbox"/>	L2		(debug\$6 with (keyboard with controller\$2))	27
<input type="checkbox"/>	L1		(debug\$6 with (keyboard controller\$2))	7

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Quick Tips

- Enter your search terms in lower case with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

- Capitalize proper nouns to search for specific people, places, or products.

John Colter, Netscape Navigator

- Enclose a phrase in double quotes to search for that exact phrase.

"museum of natural history" "museum of modern art"

- Narrow your searches by using a **+** if a search term must appear on a page.

museum +art

- Exclude pages by using a **-** if a search term must not appear on a page.

museum -Paris

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

museum +"natural history" dinosaur -Chicago



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Search: The ACM Digital Library The Guide

+abstract:debug +abstract:main +abstract:process +abstract:

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"museum of natural history" "museum of modern art"

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museum +art

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museum -Paris

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museum +"natural history" dinosaur -Chicago



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(debug<in>ab) <and> (bus<in>ab)

Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 High-speed bus debug and validation test challenges

Hosman, M.;

Electronics Manufacturing Technology Symposium, 2004. IEEE/CPMT/SEMI 29 International , July 14-16, 2004

Pages:218 - 222

[\[Abstract\]](#) [\[PDF Full-Text \(615 KB\)\]](#) **IEEE CNF**

2 A multigigahertz clocking scheme for the Pentium(R) 4 microproce

Kurd, N.A.; Barkarullah, J.S.; Dizon, R.O.; Fletcher, T.D.; Madland, P.D.;
Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 11 , Nov. 2001

Pages:1647 - 1653

[\[Abstract\]](#) [\[PDF Full-Text \(371 KB\)\]](#) **IEEE JNL**

3 Measurement system for a preliminary characterization of flash memory cells for multilevel applications

Bucci, G.; Faccio, M.; Landi, C.; Marotta, G.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 47 , Issue 5 , Oct. 1998

Pages:1385 - 1390

[\[Abstract\]](#) [\[PDF Full-Text \(212 KB\)\]](#) **IEEE JNL**

4 Constructing a multichannel electrocardiography system from a few standardized, high-level components

Malkin, R.A.;

Engineering in Medicine and Biology Magazine, IEEE , Volume: 17 , Issue: 1 , Feb. 1998

Pages:34 - 38

[\[Abstract\]](#) [\[PDF Full-Text \(1464 KB\)\]](#) [IEEE JNL](#)

5 Application of scan hardware and software for debug and diagnosis in a workstation environment

Dervisoglu, B.I.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on, Volume: 9, Issue: 6, June 1990

Pages:612 - 620

[\[Abstract\]](#) [\[PDF Full-Text \(800 KB\)\]](#) [IEEE JNL](#)

6 Field configurable system-on-chip device architecture

Knapp, S.; Tavana, D.;

Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE 2000, 21-24 May 2000

Pages:155 - 158

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) [IEEE CNF](#)

7 A flexible model for evaluating the behavior of hardware/software systems

Allara, A.; Filipponi, S.; Fornaciari, W.; Salice, F.; Sciuto, D.;

Hardware/Software Codesign, 1997. (CODES/CASHE '97), Proceedings of the International Workshop on, 24-26 March 1997

Pages:109 - 114

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8 Using background modes for testing, debugging and emulation of microcontrollers

Melear, C.;

WESCON/97. Conference Proceedings, 4-6 Nov. 1997

Pages:90 - 97

[\[Abstract\]](#) [\[PDF Full-Text \(444 KB\)\]](#) [IEEE CNF](#)

9 An integrated software platform for the design and DSP-based implementation of digital filters

Filho, M.S.; Schneebeli, H.A.; Machado, A.C.;

Circuits and Systems, 1996., IEEE 39th Midwest symposium on, Volume: 2, 21 Aug. 1996

Pages:868 - 871 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(516 KB\)\]](#) [IEEE CNF](#)

10 The design and implementation of DSP TMS320C40 parallel processor system

Wang Baoli; Li Changli; Guan Dinghua;

Signal Processing, 1996., 3rd International Conference on, Volume: 1, 14-18 1996

Pages:453 - 456 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(288 KB\)\]](#) [IEEE CNF](#)

11 Multi-GHz clocking scheme for Intel(R) Pentium(R) 4 microproces

Kurd, N.A.; Barkatullah, J.S.; Dizon, R.O.; Fletcher, T.D.; Madland, P.D.;
Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 200
IEEE International , 5-7 Feb. 2001
Pages:404 - 405

[\[Abstract\]](#) [\[PDF Full-Text \(136 KB\)\]](#) [IEEE CNF](#)

12 Reconfigurable computing at Xilinx

Guccione, S.A.;
Digital Systems, Design, 2001. Proceedings. Euromicro Symposium on , 4-6 S
2001
Pages:102

[\[Abstract\]](#) [\[PDF Full-Text \(35 KB\)\]](#) [IEEE CNF](#)

**13 A high bandwidth superscalar microprocessor for multimedia
applications**

*Raam, F.M.; Agarwal, R.; Malik, K.; Landman, H.A.; Tago, H.; Teruyama, T.;
Sakamoto, T.; Yoshida, T.; Yoshioka, S.; Fujimoto, Y.; Kobayashi, T.; Hiroi, T.
Oka, M.; Ohba, A.; Suzuoki, M.; Yutaka, T.; Yamamoto, Y.;*
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**14 Measurement system for a preliminary characterisation of flash
memory cells for multilevel applications**

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Conference Proceedings. IEEE , Volume: 1 , 18-21 May 1998
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[\[Abstract\]](#) [\[PDF Full-Text \(408 KB\)\]](#) [IEEE CNF](#)

**15 A 200 MHz 1.2 W 1.4 GFLOPS microprocessor with graphic operati
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*Nishii, O.; Arakawa, F.; Ishibashi, K.; Nakano, S.; Shimura, T.; Suzuki, K.;
Tachibana, M.; Totsuka, Y.; Tsunoda, T.; Uchiyama, K.; Yamada, T.; Hattori, T.
Maejima, H.; Nakagawa, N.; Narita, S.; Seki, M.; Shimazaki, Y.; Satomura, R.
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[\[Abstract\]](#) [\[PDF Full-Text \(1216 KB\)\]](#) [IEEE CNF](#)
